HEX6

HEX7

HEX4

HEX5

HEX2

HEX3

HEX0

HEX1

7

Hex

To

7

7

Hex

To

7

7

Hex

To

7

7

Hex

To

7

7

Hex

To

7

7

Hex

To

7

7

7

Hex

To

7

SW(7..0)

SW(7..0)

SW(15..8)

SW(15..8)

KEY3

KEY2

KEY0

FPGA